SM4311PSKP



P-Channel Enhancement Mode MOSFET

Features

• -30V/-100A,

 $R_{DS(ON)} = 2.8 m\Omega(max.) @ V_{GS} = -10V$ $R_{DS(ON)} = 4.3 m\Omega(max.) @ V_{GS} = -4.5V$

- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- HBM ESD Capability level of 6.6KV typical

Note : The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

Applications

 Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Pin Description







P-Channel MOSFET

Ordering and Marking Information

SM4311PS DD-DD D Assembly Materia Handling Code Temperature Rar Package Code	C:-55 to 150 °C Handling Code
SM4311PS KP : SM4311 XXXXX	XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.



Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit		
Common	Ratings (T _A =25°C Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		-30		
V_{GSS}	Gate-Source Voltage		±20	V	
TJ	Maximum Junction Temperature		150	_ ℃	
T _{STG}	Storage Temperature Range		-55 to 150	J°C	
ls	Diode Continuous Forward Current	T _C =25°C	-100 ^ª		
1	Continuous Drain Current	T _C =25°C	-100 ^a		
Ι _D	Continuous Drain Current T _C =100°C		-100 ^a	— A	
I _{DM}	Pulsed Drain Current	T _C =25°C	-400 ^b		
D	Meximum Dever Dissinction	T _C =25°C	125	14/	
PD	Maximum Power Dissipation	T _C =100°C	50	W	
$R_{ ext{ heta}JC}$	Thermal Resistance-Junction to Case	Steady State	1	°C/W	
1	Continuous Drain Current	T _A =25°C	-41.8		
Ι _D	Continuous Drain Current		-33.5	A	
PD	Meximum Device Diseinction	T _A =25°C	7.4	14/	
ГD	Maximum Power Dissipation $T_A=70^{\circ}C$		4.7	— W	
	Thermal Desistance, lunction to Ambient	t ≤ 10s	17	°C AA	
$R_{ ext{ heta}JA}$	Thermal Resistance-Junction to Ambient	Steady State	55	- °C/W	
I _{AS} ^c	As ^c Avalanche Current, Single pulse (L=0.5mH)		25	Α	
E _{AS} ^c	Avalanche Energy, Single pulse (L=0.5mH)		156.25	mJ	

Note a : Package is limited to 100A.

Note b : Pulse width limited by max. junction temperature.

Note c : UIS tested and pulse width limited by maximum junction temperature 150° C (initial temperature $T_j=25^{\circ}$ C).

Electrical Characteristics ($T_A = 25^{\circ}C$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-30	-	-	V
I _{DSS} Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	-	-	-1		
		T _J =85°C	-	-	-30	μA
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1.3	-1.8	-2.3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^d Di	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-25A	-	2.2	2.8	
		V _{GS} =-4.5V, I _{DS} =-20A	-	3.1	4.3	mΩ



Electrical Characteristics (Cont.) (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Diode Cha	aracteristics			•	•	
$V_{\text{SD}}^{\ \text{d}}$	Diode Forward Voltage	I _{SD} =-1A, V _{GS} =0V	-	-0.7	-1	V
t _{rr} ^e	Reverse Recovery Time	I _{SD} =-40A,	-	46	-	ns
Q _{rr} ^e	Reverse Recovery Charge	di _{sD} /dt=100A/µs	-	52	-	nC
Dynamic	Characteristics ^e				•	
R _g	Gate Resistance	V _{GS} =0V, V _{DS} =0V,F=1MHz	-	1.8	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V,	-	8900	-	
C _{oss}	Output Capacitance	V _{DS} =-15V, Frequency=1.0MHz	-	1560	-	pF
C _{rss}	Reverse Transfer Capacitance		-	1160	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	28	-	
t _r	Turn-on Rise Time		-	26	-	-
$t_{d(OFF)}$	Turn-off Delay Time		-	260	-	ns
t _f	Turn-off Fall Time		-	135	-	
Gate Cha	rge Characteristics ^e				•	
Q_g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-40A	-	186	-	
Q_{gs}	Gate-Source Charge		-	22	-	nC
Q_{gd}	Gate-Drain Charge		-	42	-	

Note d : Pulse test ; pulse width \leq 300µs, duty cycle \leq 2%.

Note e : Guaranteed by design, not subject to production testing.



Typical Operating Characteristics



120 100 80 60 40

80

T_i - Junction Temperature (°C)

Thermal Transient Impedance

100 120 140 160

′С,V_G=-10V

60

-l_b - Drain Current (A)

20

0

0

T_c=25

20 40

Drain Current



Safe Operation Area



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Typical Operating Characteristics (Cont.)



Drain-Source On Resistance



Gate-Source On Resistance

Gate Threshold Voltage





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Typical Operating Characteristics (Cont.)



Drain-Source On Resistance

Source-Drain Diode Forward



Capacitance



Gate Charge



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Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



SM4311PSKP



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Classification Profile





Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time $(T_{smin}$ to $T_{smax})$ (t _s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds	
Average ramp-up rate (T _{smax} to T _P)	3 °C/second max.	3°C/second max.	
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds	
Peak package body Temperature (T _p)*	See Classification Temp in table 1	See Classification Temp in table 2	
Time $(t_P)^{**}$ within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds	
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.	
Time 25°C to peak temperature	6 minutes max.	8 minutes max.	
* Tolerance for peak profile Temperature (T _p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t _p) is defined as a supplier minimum and a user maximum.			

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ <350	Volume mm ^³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm ³	Volume mm ³	Volume mm ³
Thickness	<350	350-2000	>2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ Tjmax
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ Tjmax
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
ТСТ	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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